REMARKS/ARGUMENTS

Reconsideration and withdrawal of the rejections of the application are respectfully requested in view of the amendments and remarks herewith, which place the application into condition for allowance. The present amendment is being made to facilitate prosecution of the application.

I.   STATUS OF THE CLAIMS AND FORMAL MATTERS

Claims 1, 4, 5, 8, 9 and 12 are currently pending. Claims 1, 5, and 9, which are independent, are hereby amended. Claims 2-3, 6-7, and 10-11 are hereby canceled, without prejudice or disclaimer of subject matter. No new matter has been introduced. Support for this amendment is provided throughout the Specification as originally filed, and specifically at pages 12-13 and page 14, lines 19-23.

Changes to claims are not made for the purpose of patentability within the meaning of 35 U.S.C. §101, §102, §103, or §112. Rather, these changes are made simply for clarification and to round out the scope of protection to which Applicants are entitled.

II.   REJECTIONS UNDER 35 U.S.C. §102(e)

Claims 1-12 were rejected under 35 U.S.C. §102(e) as allegedly anticipated by U.S. Patent No. 6,760,888 to Killian (hereinafter, merely "Killian").

As understood by Applicants, Killian relates to an automated processor design tool that uses a description of customized processor instruction set extensions in a standardized language to develop a configurable definition of a target instruction set, a Hardware Description Language description of circuitry necessary to implement the instruction set, and development

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tools such as a compiler, assembler, debugger and simulator which can be used to develop

applications for the processor and to verify it. Implementation of the processor circuitry can be

optimized for various criteria such as area, power consumption, speed and the like.

        Claim 1 recites, *inter alia*:

> A digital signal processing apparatus, comprising:
>
> **a host processing block for controlling entire of said
> digital signal processing apparatus through outputting a
> command that does not depend on hardware and that is not on
> real time basis;**
>
> **a plurality of digital signal processing blocks each
> having means for interpreting said command from said host
> processing block and for controlling a hardware component;**
>
> a bus for connecting said host processing block and said
> plurality of digital signal processing blocks; and
>
> an interface for an extension function providing medium
> connected to said bus..." (Emphasis added)

Applicants respectfully submit that nothing has been found in Killian that would

teach or suggest the above identified features of claim 1. Specifically, Killian fails to disclose or

suggest <u>a host processing block for controlling entire of said digital signal processing apparatus</u>

<u>through outputting a command that does not depend on hardware and that is not on real time</u>

<u>basis</u>. Furthermore, Killian does not disclose or suggest <u>a plurality of digital signal processing</u>

<u>blocks each having means for interpreting said command from said host processing block and for</u>

<u>controlling a hardware component</u>, all as recited in claim 1.

        Therefore, Applicants respectfully submit that claim 1 is patentable.

Independent claims 5 and 9 are similar in scope, and are therefore patentable for similar reasons.

III.   DEPENDENT CLAIMS

Claims 4, 8, and 12 are dependent from one of the independent claims, discussed above, and are therefore patentable for at least the same reasons. Since each dependent claim is also deemed to define an additional aspect of the invention, however, the individual reconsideration of the patentability of each on its own merits is respectfully requested.
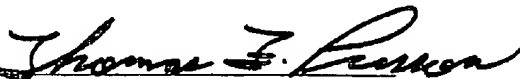
CONCLUSION

In the event the Examiner disagrees with any of statements appearing above with respect to the disclosure in Killian, it is respectfully requested that the Examiner specifically indicate those portions of Killian providing the basis for a contrary view.

Please charge any additional fees that may be needed, and credit any overpayment, to our Deposit Account No. 50-0320.

In view of the foregoing amendments and remarks, it is believed that all of the claims in this application are in condition for allowance and Applicants respectfully request early passage to issue of the present application.

Respectfully submitted,

FROMMER LAWRENCE & HAUG LLP
Attorneys for Applicants

By _____
Thomas F. Presson
Reg. No. 41,442
(212) 588-0800